

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Srinivasan T. RAJAPPA, et al.

Application No. 10/813,145

Filed: March 30, 2004

For: A METHOD AND APPARATUS FOR A LOW LATENCY SOURCE-SYNCHRONOUS ADDRESS RECEIVER FOR A HOST SYSTEM BUS IN A MEMORY CONTROLLER

Examiner: Hetul B. Patel

Art Unit 2186

AMENDMENT AND RESPONSE TO OFFICE ACTION

Mail Stop Amendment Commissioner for Patents P. O. 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed December 23, 2004, Applicants respectfully request entry of the following amendments in connection with the above-identified application: